

This United States Application entitled "Printed Circuit Board and Method for Use Thereof" claims priority to British Application No. 0304440.1 filed 2 February 2003.

## **BACKGROUND OF THE INVENTION-**

**Page 1, replace the paragraph beginning at line 10 with the following:**

It is generally the case that one or more functions of the PCB are tested prior to the PCB being assembled in an apparatus or being shipped to a customer. One or more test ports are typically provided on one or more surfaces of the PCB to allow the connection of testing equipment thereto. However, the provision of these test ports also allows third parties to subsequently analyze the PCB and/or the electrical components thereon, thereby allowing them to gain unauthorized information relating to the control logic and/or control system associated therewith.

**After line 19 on Page 1 insert the following header:**

## **--SUMMARY OF THE INVENTION--**

**Page 2, replace the paragraph beginning at line 8 with the following:**

The advantage of the present invention is that by allowing removal of the portion of the PCB having the one or more test ports thereon (hereinafter referred to as the testing port portion) from the main body of the PCB, this prevents or at least reduces the likelihood of third parties being able to gain unauthorized access to the control systems of the PCB or electrical apparatus in which the PCB is mounted.

**Page 3, replace the paragraph beginning at line 12 with the following:**

The PCB can be single sided or double sided and preferably the PCB is a multi-layered board (i.e., two or more surfaces having a number of electrical components and/or electrically conductive tracks associated therewith) since if the electrically conductive tracks communicating with the one or more test ports are provided on an inner layer, it will be difficult, if not impossible, for a third party to gain access to the tracks. The tracks of single layered or double sided boards may be accessed more easily by an unauthorized third party.

**Page 4, replace the paragraph beginning at line 15 with the following:**

Thus the present invention provides a novel way of providing a connector for a PCB for development and production testing during manufacture and which can be easily removed to prevent unauthorized modification to the control system provided, at least in part, by the PCB.

**Before line 19 on Page 4 insert the following header:**

**--DESCRIPTION OF THE DRAWINGS--**

**Page 5, before line the first line, insert the following header:**

**--DESCRIPTION OF THE PREFERRED EMBODIMENTS--**

**Page 5, replace the paragraph beginning at line 14 with the following:**

In accordance with the present invention, the end portion 10 is connected to a main body portion 16 of the PCB via a frangible portion 14, thereby allowing end portion 10 to be snapped off from the

remaining portion 16 of the PCB once one or more of the test ports have been used to test the PCB. Once the test ports are removed, this prevents third parties from gaining access to the control systems of the PCB. As such, end portion 10 provides a removable connector for allowing testing of the PCB whilst meeting the requirements set by encryption organizations and/or similar bodies.

**Page 6 after the last line, insert the following new paragraph:**

While the invention has been described with a certain degree of particularity, it is manifest that many changes may be made in the details of construction and the arrangement of components without departing from the spirit and scope of this disclosure. It is understood that the invention is not limited to the embodiments set forth herein for purposes of exemplification, but is to be limited only by the scope of the attached claim or claims, including the full range of equivalency to which each element thereof is entitled.